

72
Amended
shown in the top view of Fig. 3f. Thus, a stacked-gate as shown in the cross-sectional view of Fig. 3g, is formed.

IN THE CLAIMS

Please amend claim 29 as follows:

Sub 10/1
29. (AMENDED TWICE) A stacked-gate flash memory having a shallow trench isolation with a high-step oxide and high lateral coupling comprising:

63
a substrate having a gate oxide layer;

at least two trenches formed to a depth between about 2500 to 5000 Å below the surface of said substrate;

an oxide layer formed over said substrate, including over the inside walls of said two trenches;

12
a high-step oxide formed within said two trenches over said oxide liner and protruding upward over the surface of said substrate to a height between about 2000 to 6000 Å;

said high-step oxide forming an opening with high walls

Sub
18
cont
18 over the surface of said substrate between said two
trenches;

13
21 a first conductive layer formed conformally inside said
opening and over the surface of the substrate between said
high walls to form a floating gate having folding surfaces;

General
27
24 an intergate oxide formed over said floating gate having
folding surfaces;

27 a second conductive layer formed protruding downward in
between said folding surfaces over said intergate oxide
30 layer to form a control gate; and

a self-aligned source (SAS) line.

33